

PATENT NUMBER

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APPLICATION NO. 09/683486	CONT/PRIOR	CLASS 257	SUBCLASS 341	ART UNIT 2811	EXAMINER G. E. Brennan
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TITLE	APPLICANTS
1. Chief of Police	1. [Name]
2. [Title]	2. [Name]
3. [Title]	3. [Name]
4. [Title]	4. [Name]
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99. [Title]	99. [Name]
100. [Title]	100. [Name]

Andres Bryant
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Edward Nowak

Fin-based double poly dynamic threshold CMOS FET with spacer gate and method of fabrication

PTO-2040
12/88

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CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
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